

<b>Notice of References Cited</b>	Application/Control No. 10/736,720	Applicant(s)/Patent Under Reexamination LEE, KEUN WOO	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,495,424	12-2002	Kunikiyo, Tatsuya	438/296
	B	US-2004/0178430	09-2004	Rhodes et al.	257/292
	C	US-6,635,532	10-2003	Song et al.	438/259
	D	US-6,177,333	01-2001	Rhodes, Howard E.	438/433
	E	US-6,670,666	12-2003	Shimizu, Satoshi	257/298
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 209-210, 325.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.